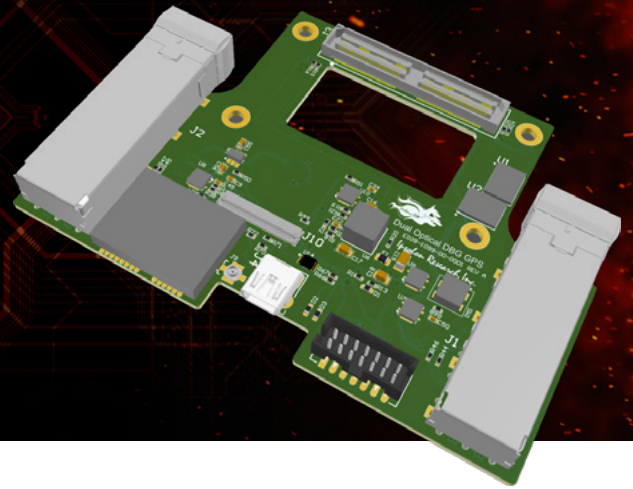


Dual Optical Module

Provides IO Expansion and software development support to the Cerberus and Chameleon SDR



FEATURES

UART over USB for Linux or bare metal software development

JTAG for ARM MPU or FPGA image loading and debugging

MPU/FPGA reset button

1G Ethernet

Single or dual 10G optical transceivers - connect directly to SoC FPGA MGTs when plugged into Cerberus or Chameleon SDRs

QSPI Flash Memory supports Cerberus SDR boot and file system

OPTIONAL GPS DISCIPLINED CLOCK REFERENCE

GPS subsystem provides geolocation to Cerberus/Chameleon

External GPS antenna connector

Concurrent reception of GPS/QZSS, GLONASS, BeiDou

Autonomous 100 ppb hold-over

GPS locked clock references

Integral disciplined low phase-noise 30.72 MHz system reference oscillator

Optionally GPS lock Cerberus VCXO

Cerberus VCXO is standard 100MHz or user specified

Locks all Cerberus ADC/DAC channels and RFFE synthesizers for true phase coherent systems

Dual Optical Module Mated to Cerberus SDR

